

FIG.1

FIG.2

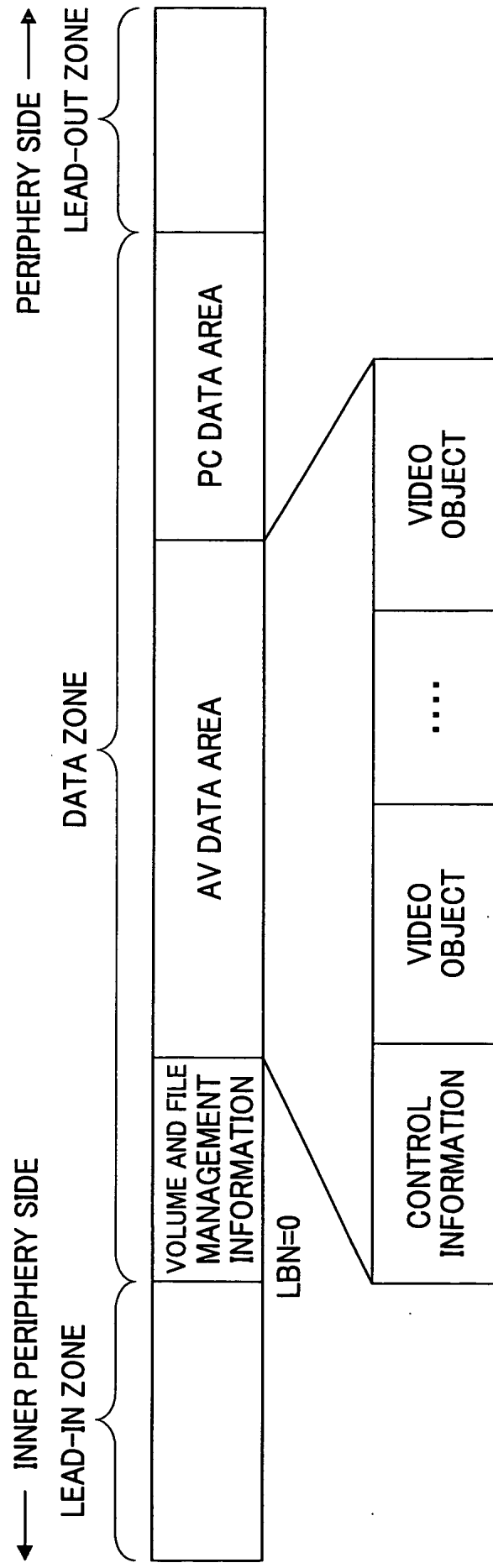


FIG.3

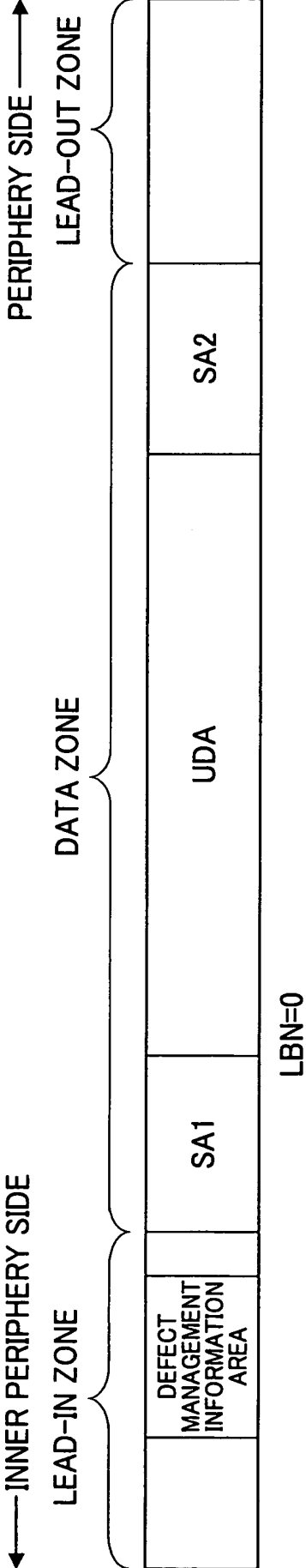


FIG.4

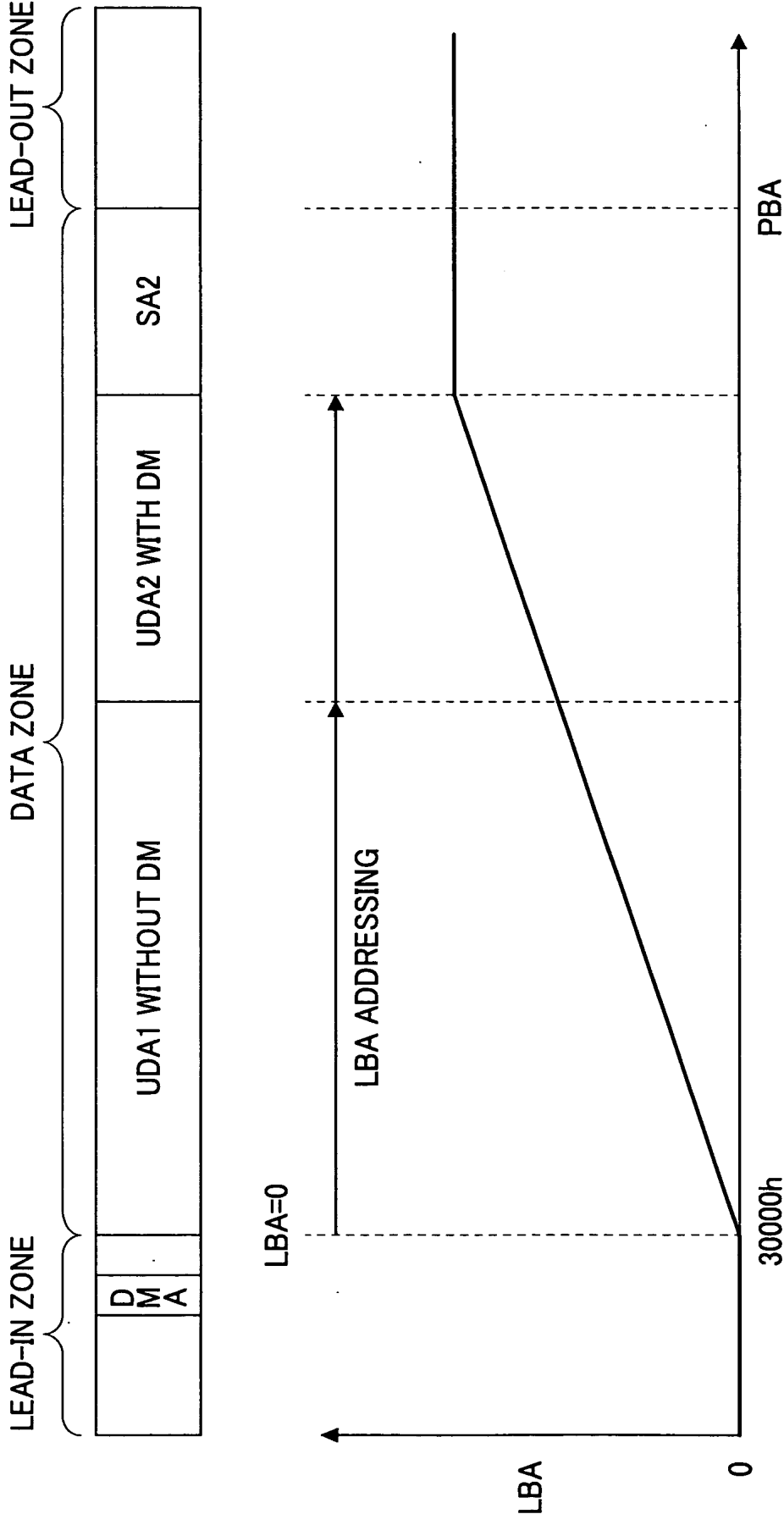


FIG.5

| BP in Block | Contents | Length in bytes |
|--------------|--|-----------------|
| 0 | Signature of the DMI ("DMI") | 3 |
| 3 | Version number | 1 |
| 4 | DMI update count | 4 |
| 8 | UDA1 start address pointer | 4 |
| 12 | UDA1 end address pointer | 4 |
| 16 | UDA2 start address pointer | 4 |
| 20 | UDA2 end address pointer | 4 |
| 24 | SA2 size | 4 |
| 28 | Number of Replacement List (RPL) entries = N | 4 |
| 32 | RPL entry 0 | 8 |
| 40 | RPL entry 1 | 8 |
| | | |
| (N-1) × 8+32 | RPL entry N | 8 |

FIG.6

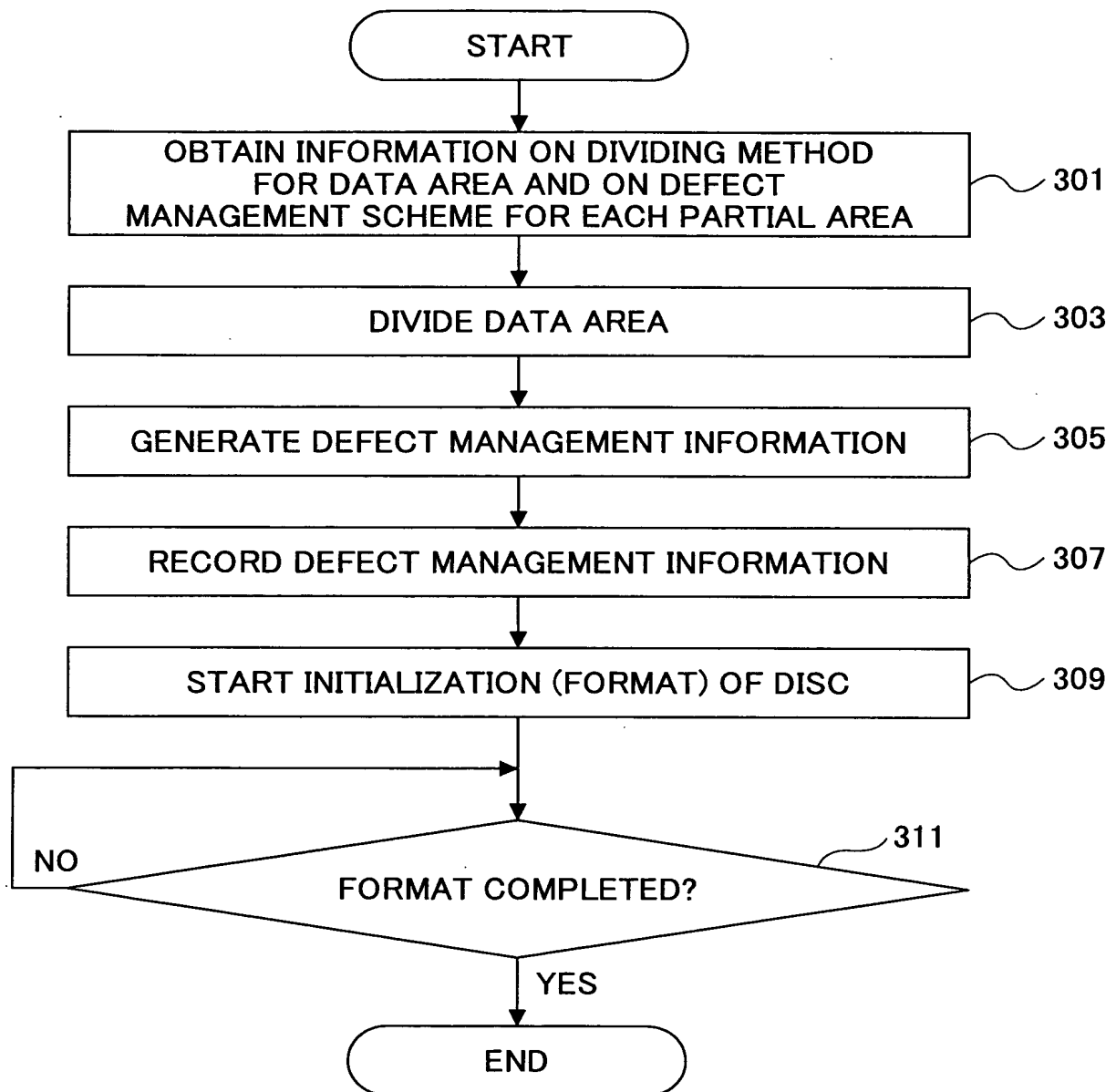


FIG.7

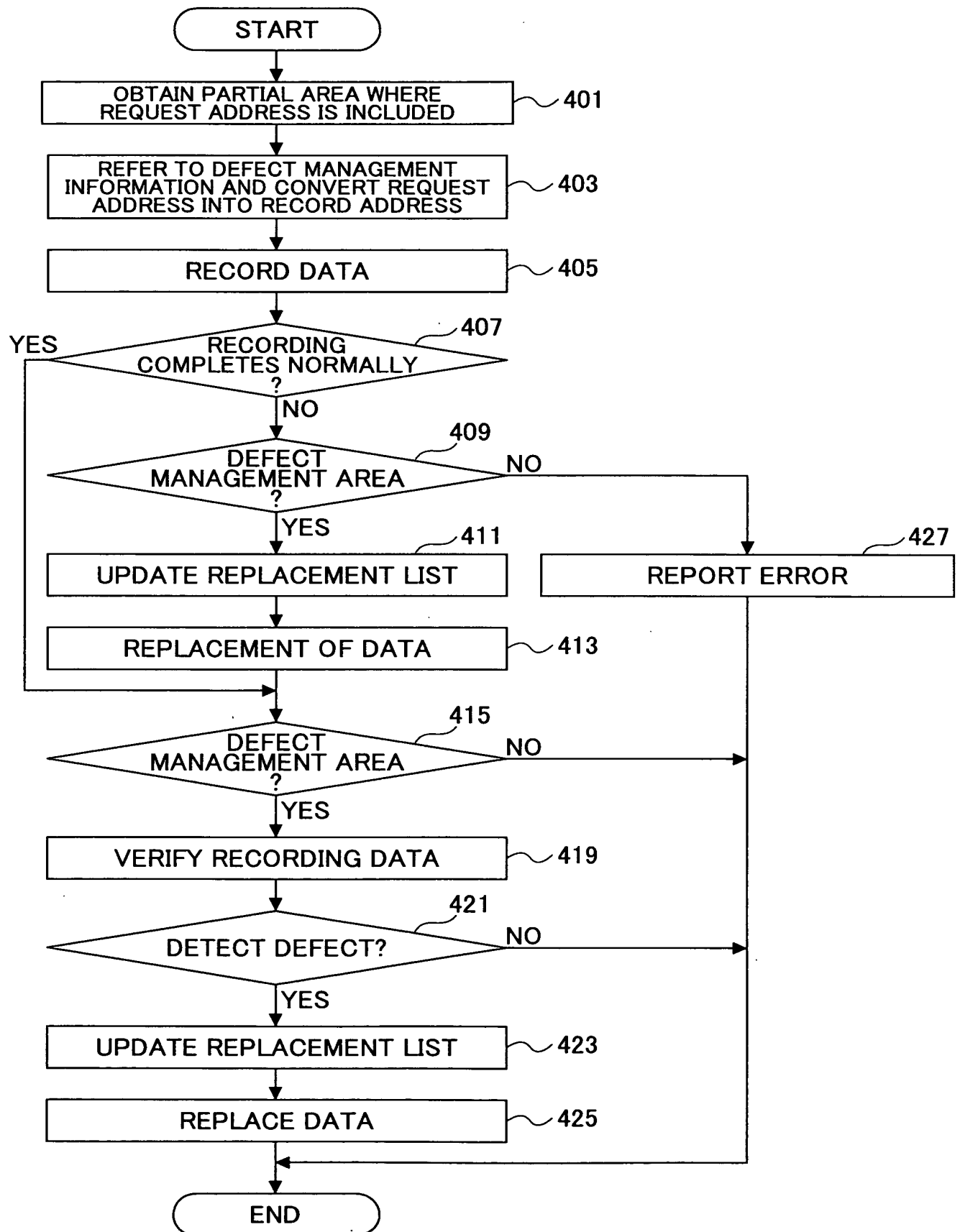


FIG.8

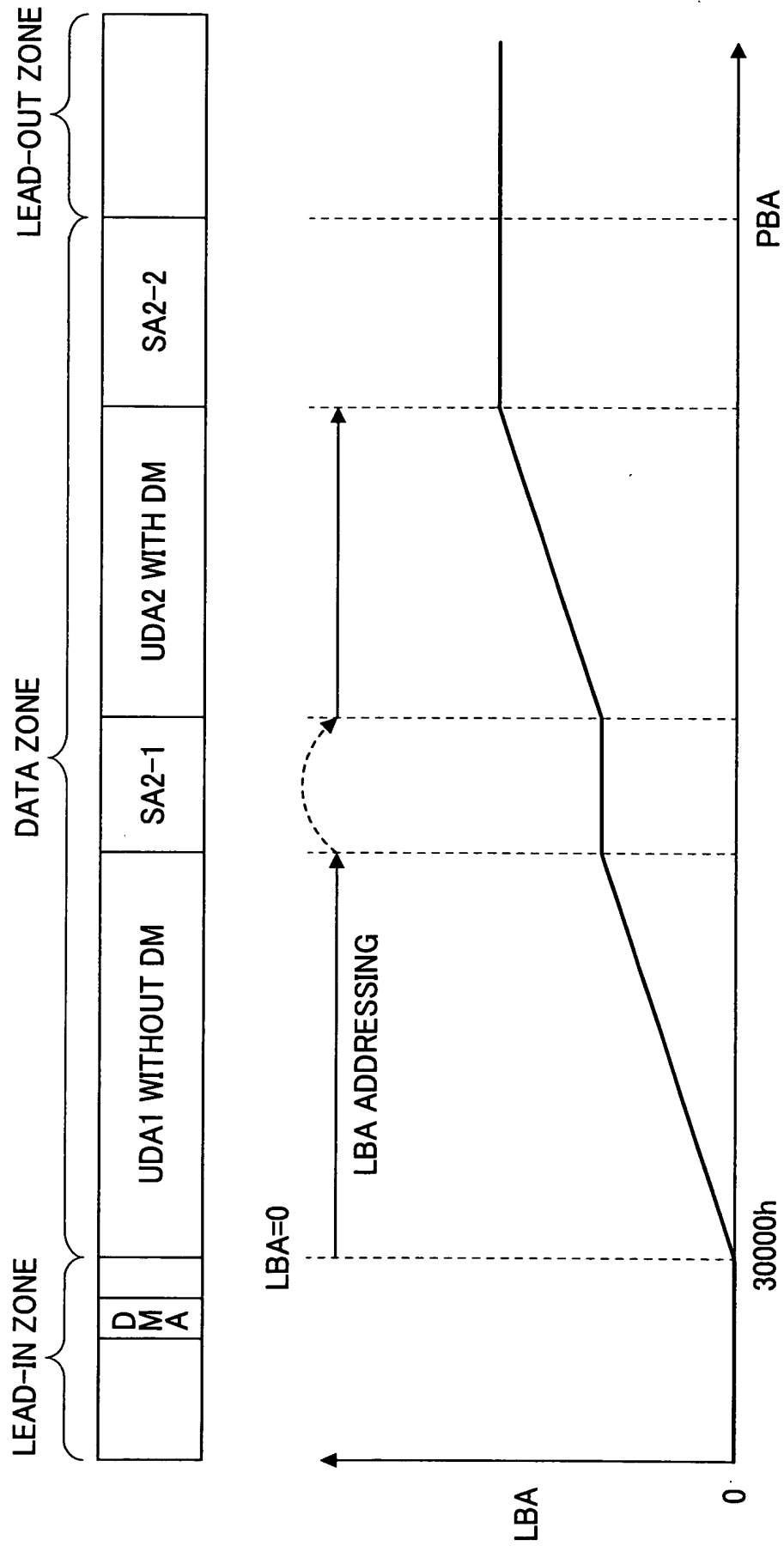


FIG.9

| BP in Block | Contents | Length in bytes |
|---------------------|------------------------------|-----------------|
| 0 | Signature of the DMI ("DMI") | 3 |
| 3 | Version number | 1 |
| 4 | DMI update count | 4 |
| 8 | Number of RPL Blocks = 2 | 2 |
| 10 | Reserved | 6 |
| 16 | RPL Block 1 | 32 |
| 48 | RPL Block 2 | 32 |
| 80 | RPL2 entry 0 | 8 |
| 88 | RPL2 entry 1 | 8 |
| | | |
| $(N-1) \times 8+80$ | RPL2 entry N | 8 |

FIG.10

| BP in Block | Contents | Length in bytes |
|-------------|------------------------------------|-----------------|
| 0 | Signature of the RPL Block ("RPL") | 3 |
| 3 | RPL Block number | 1 |
| 4 | UDA start address pointer | 4 |
| 8 | UDA end address pointer | 4 |
| 12 | SA-1 size | 4 |
| 16 | SA-2 size | 4 |
| 20 | Number of RPL entries | 4 |
| 24 | Location of RPL entry 0 | 2 |
| 26 | Reserved | 6 |

FIG.11

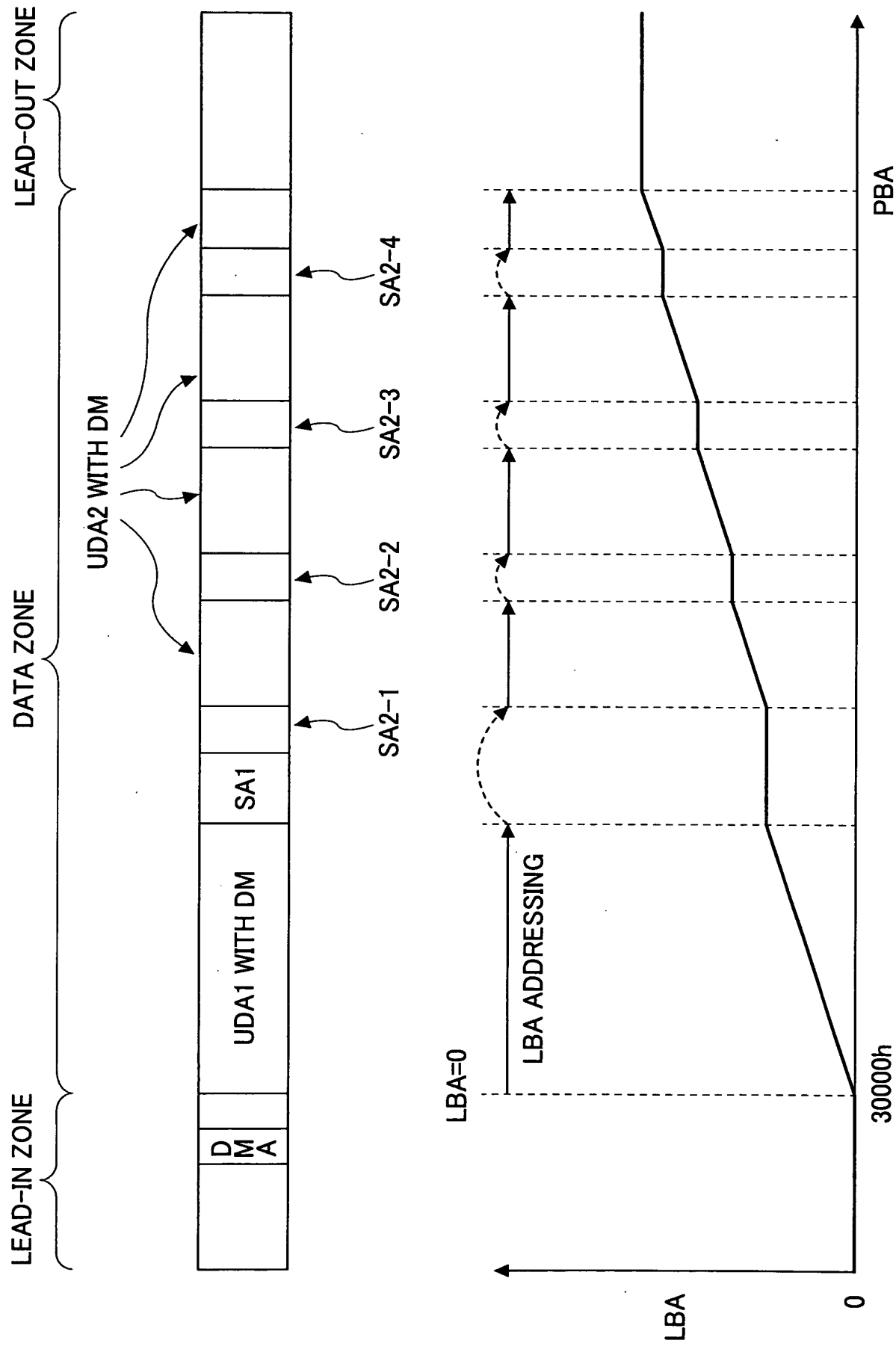


FIG.12

| BP in Block | Contents | Length in bytes |
|---|------------------------------|-----------------|
| 0 | Signature of the DMI ("DMI") | 3 |
| 3 | Version number | 1 |
| 4 | DMI update count | 4 |
| 8 | Number of RPL Blocks = 2 | 2 |
| 10 | Reserved | 6 |
| 16 | RPL Block 1 | 64 |
| 80 | RPL Block 2 | 64 |
| 144 | RPL1 entry 0 | 8 |
| 152 | RPL1 entry 1 | 8 |
| | | |
| $(N-1) \times 8 + 144$ | RPL1 entry N1 | 8 |
| $(N-1) \times 8 + 8 + 144$ | RPL2 entry 0 | 8 |
| $(N-1) \times 8 + 16 + 144$ | RPL2 entry 1 | 8 |
| | | |
| $(N1-1) \times 8 + (N2-1) \times 8 + 144$ | RPL2 entry N2 | 8 |

FIG.13

| BP in Block | Contents | Length in bytes |
|-------------|------------------------------------|-----------------|
| 0 | Signature of the RPL Block ("RPL") | 3 |
| 3 | RPL Block number | 1 |
| 4 | RPL Block type | 1 |
| 5 | Reserved | 3 |
| 8 | UDA start address pointer | 4 |
| 12 | UDA end address pointer | 4 |
| 16 | Number of Replacement List entries | 4 |
| 20 | Location of RPL entry 0 | 2 |
| 22 | Reserved | 10 |
| 32 | RPL Block specific data | 32 |

FIG.14

